PRODUCT SPECIFICATIONS

For Customer:			☐ : APPROVAL FOR SPECIFICATION				
Customer Model No			_ □ : APPROVAL FOR SAMPLE				
Module No.: BD-T101BAH-01			Date : 20	14-12-04			
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Approve	ed By		Comme	ent			
PREPA	ARED	CHECKED	VERIFIED BY QA DEPT		VERIFIED BY R&D DEPT		
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2. Revision Record

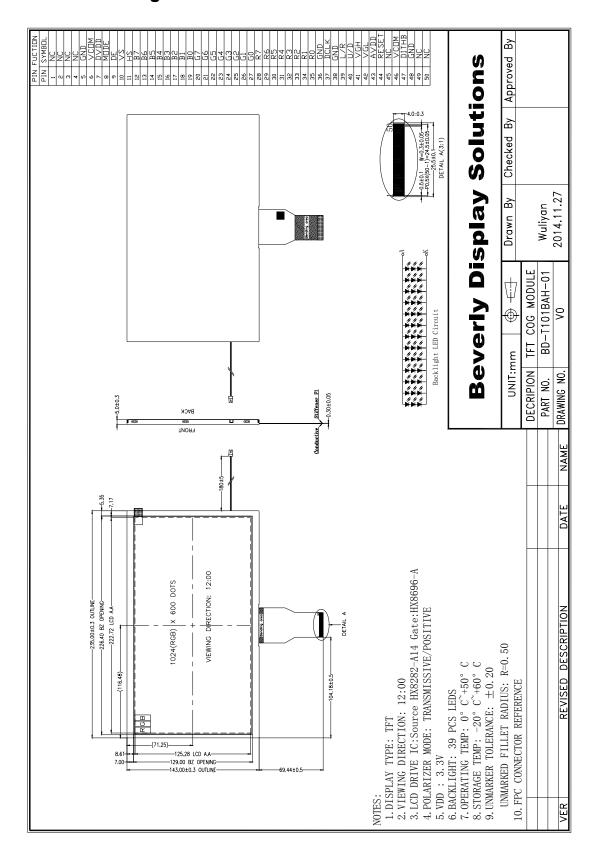
Date	Rev.No.	Page	Revision Items	Prepared
2014-12-04	V0		The first release	ZHP

3. General Specifications

BD-T101BAH-01 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The $10.1^{\prime\prime}$ display area contains 1024×600 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	12	O'Clock	
Gray scale inversion direction	6	O'Clock	
Operating temperature	-20~+70	$^{\circ}$	
Storage temperature	-30~+80	$^{\circ}$	
Module size	Refer to outline drawing	mm	
Active Area(W×H)	222.27X125.28	mm	
Number of Dots	1024×600	dots	
Controller	HX8282A14+HX8696	-	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	3X13-LEDs (white)	pcs	
Weight		g	
Interface	RGB888	-	

4. Outline Drawing



5. Absolute Maximum Ratings(Ta=25℃)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note
	V_{DD}	-0.3	5.0	V	1, 2
	AV_DD	6.5	13.5	V	
Power Supply Voltage	V_{GH}	-0.3	42.0	V	
	V_{GL}	-20	0.3	V	
	V_{GH} - V_{GL}	-	40.0	V	

Notes:

- If the module is above these absolute maximum ratings. It may become permanently damaged.
 Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
- 2. $V_{CC} > V_{SS}$ must be maintained.

5.2 Typical operation conditions

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
	VDD	3.0	3.3	3.6	V	
Power veltage	AVDD	10.2	10.5	10.8	V	
Power voltage	VGH	20	21	22	V	
	VGL	-5.0	-5.5	-6.0	V	
Input signal voltage	VCOM	3.3	3.7	4.2	V	
Input logic high voltage	VIH	0.7 VDD	_	VDD	V	
Input logic low voltage	VIL	0	-	0.3VDD	V	

5.3 Environmental Absolute Maximum Ratings.

Item	Stor	age	Opera	Note	
item	MIN.	MAX.	MIN.	MAX.	Note
Ambient Temperature	-30℃	80℃	-20℃	70℃	1,2
Humidity	-	-	-	-	3

- 1. The response time will become lower when operated at low temperature.
- 2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40°C:85%RH MAX.

Ta>=40°C:Absolute humidity must be lower than the humidity of 85%RH at 40°C.

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

Paramet	Parameter Symbol		Condition	Min	Тур	Max	Unit	Note
Power sup	pply	VCC	Ta=25°C	3.0	3.3	3.6	V	
Input	'H'	V_{IH}	V _{CC} =3.3V	0.8V _{CC}	-	V _{CC}	V	
voltage	'L'	V _{IL}	V _{CC} =3.3V	0	-	0.2V _{CC}	V	
Curren	t	I _{CC1}	Normal mode	-	25	50	mA	2
Consumption		I _{CC2}	Sleep mode	-	0.05	0.1	mA	2
Clock Frequen		f clk	-	-	30	50	MHz	

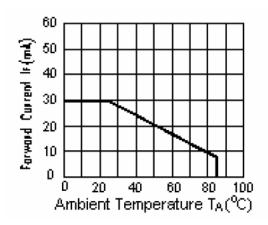
Note:

^{1:}When an optimum contrast is obtained in transmissive mode.

^{2:} Tested in 1×1 chessboard pattern.

6.2 LED backlight specification(VSS=0V ,Ta=25 $^{\circ}$ C)

Item	Symb ol	Condition	Min	Тур	Max	Unit	Note
Supply voltage	Vf	If=20X13mA	1	9.0	1	V	
Uniformity	∆Вр	If=20X13mA	75			%	
Luminance for LCD	Lv	If=20X13mA	-	380		Cd/m2	
Life Time	Т	If=20X13mA	-	30000		Hours	



ILED VS TEMP

6.3 Interface signals

Pin No.	Symbol	I/O	Function
1-2	VLED+	Р	LED back light(Anode)
3-4	VLED-	Р	LED back light(Cathode)
5	GND	Р	Ground.
6	VCOM	I	VCOM input
7	VDD	Р	Power for Digital Circuit
8	MODE	ı	DE or HV mode control
9	DE	1	Data Enable
10	VS	1	Vsync signal input
11	HS	1	Hsync signal input
12-19	B7-B0	1	Blue data inpu
20-27	G7-G0	1	Nomal diplay and Standby mode select pin
28-35	R7-R0	- 1	Frame sync signal
36	GND	Р	Power ground
37	DCLK	I	Sample clock
38	GND	Р	Power ground
39	L/R	I	Select left to right scanning direction
40	U/D	I	Select up or down scanning direction
41	VGH	1	Positive power for scan driver
42	VGL	1	Negative power for scan driver
43	AVDD	Р	Power for Analog Circuit
44	RESET	I	Reset
45	NC	-	No Connector
46	VCOM	I	VCOM input
47	DITHB	I	Dithering function enable control.
48-50	NC	-	No connection.

NOTE:

Setting of scan co	ntrol input	Scanning direction		
U/D	L/R			
DV _{DD}	DV _{DD}	Up to down, left to right		
GND	DV _{DD}	Down to up, left to right		
DV_DD	GND	Up to down, right to left		
GND	GND	Down to up, right to left		

7. Optical Characteristics

Item	Sy	mbol	Condition	Min.	Тур.	Max.	Unit	Note
Brightness	I	Вр	<i>θ</i> =0°	-	380	-	Cd/m ²	1
Uniformity	_	∃Bp	Ф=0°	75	-	-	%	1,2
	3	:00		-	60	-		
Viewing	6	:00	C=>10	-	45	-	1	
Angle	9	:00	Cr≥10	-	60	-	Deg	3
	1:	2:00		-	60	-		
Contrast Ratio		Cr	<i>θ</i> =0°	300	500		1	4
Response		Tr	Φ=0°	-	10	-	ms	5
Time		T _f		-	10	-	ms	
	W	х			0.28		-	
	VV	У			0.33		-	
	R	х			0.51		-	
Color of CIE	K	у			0.34		ı	
Coordinate	G	х	<i>θ</i> =0° Φ=0°		0.31		ı	1,6
	G	У	Ψ=0		0.56		-	
	В	х			0.15		-	
		у			0.14		-	-
NTSC Ratio		S		50	60	-	%	

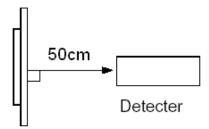
Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25℃.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

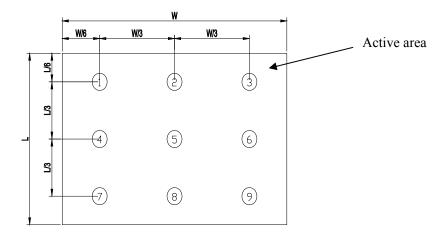


Note 2: The luminance uniformity is calculated by using following formula.

$$\triangle$$
Bp = Bp (Min.) / Bp (Max.)×100 (%)

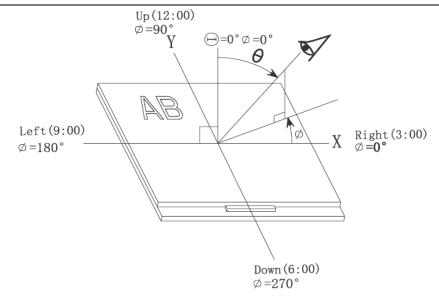
Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots.

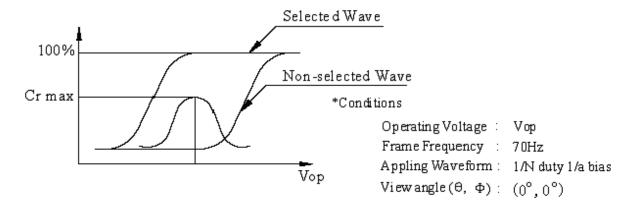


Note 3: The definition of viewing angle:

Refer to the graph below marked by θ and Φ



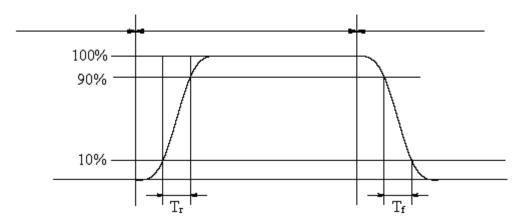
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



$$Contrast \ ratio(Cr) = \frac{Brightness \ of \ selected \ dots}{Brightness \ of \ non-selected \ dots}$$

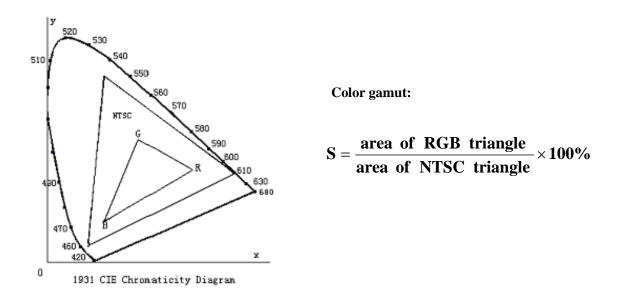
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



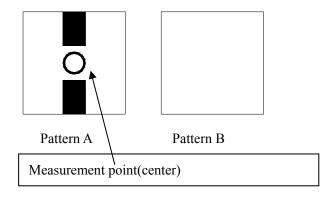
The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.



Note 7: Definition of cross talk.

Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100



Electric volume value=3F+/-3Hex

8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion	
1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off		
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off		
3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	After testing, cosmetic and electrical defects should not	
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	happen. 2. Total current consumption should	
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	not be more than twice of initial value.	
6	Temperature Cycle	-30°C		
7	Vibration Test	10Hz~150Hz, 100m/s², 120min	Not allowed cosmetic	
8	Shock Test	Half- sine wave,300m/s ² ,11ms	and electrical defects.	

Note: Operation: Supply 2.8V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9 Quality level

9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects(such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

9.2 Definition of inspection range

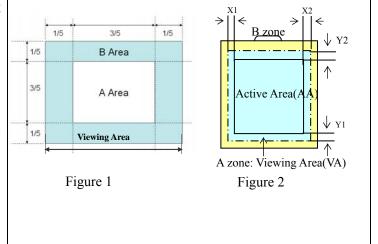
For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).

A area: center of viewing area
B area: periphery of viewing area
C area: Outside viewing area

For other defects, dividing two areas to make a judgment (according figure 2).

A zone : Inside Viewing area B zone : Outside Viewing area

X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm



9.3 Inspection items and general notes

0.0 111	3.5 mapection items and general notes					
General notes	Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and our company. Viewing area should be the area which our company guarantees. Limit sample should be prior to this Inspection standard. Viewing judgment should be under static pattern. Inspection conditions Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)					
	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder				
Inspection items	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage				
	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass				

Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display
Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction
Glass defect	Glass crack, Shaved corner of glass, Surplus glass
PCB defect	Components assembly defect

9.4 Outgoing Inspection level

Outgoing Inspection	Inspection conditions	Inspection				
standard	mapection conditions	Min.	Max.	Unit	IL	AQL
Major Defects	See 9.3 general notes		See 9.5			0.65
Minor Defects	S	See 9.	5	II	0.65	
Note: Sampling standard conforms to GB2828						

9.5 Inspection Items and Criteria

		Judgment standard				
	Inspec	ection items		Category	Acceptable number	
				Category	A zone	B zone
			Α	Ф<=0.10	Neglected	
	Black spot, White spot,	$ $ $ $ $ $ $ $ $ $ $ $	В	0.10<Ф<=0.2	1	
1	Pinhole, Foreign Particle, Particle	a	С	0.2<Ф	0	Neglected
	in or on glass, Scratch on glass	$\Phi = (a+b)/2(m$	D	-	-	
			То	tal defective point(B,C)	1	
	Black line, White Width		А	W<=0.02	Neglected	
			В	0.02 <w<=0.03 L<=1.0</w<=0.03 	1	
2	line, and Particle Between Polarizer and glass, Scratch on glass	С	0.03 <w<=0.05 L>1.0</w<=0.05 	0	Neglected	
			D	0.05 <w, 1.0<l<="" td=""><td>0</td><td></td></w,>	0	
			То	tal defective point(B,C)	1	
3	Bright spot		any size		none	none

			Α	Ф<0.2	Neglected	
		b	В	0.2<Ф<=0.3	2	
4	Contrast	\longleftrightarrow	С	0.3<Ф<=0.4	1	d
_	variation	$\Phi = (a+b)/2 \text{(mm)}$	D	0.4<Ф	0	
			То	tal defective point(B,C)	3	
5	Bubble inside cell			any size	none	none
	Polarizer defect	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Ref	fer to item 1 and item 2.		
6	(if Polarizer is used)	Bubble, dent and convex	Α	Ф<=0.1	Neglected	
			В	0.1 <Ф<=0.2	1	Neglecte d
			С	0.2 <Ф	0	_
7	Surplus glass	Stage surplus glass Surrounding surplus glass	B<=0.3mm Should not influence outline dimension and assembling		sembling.	
8	Open segment or o	open common	No	t permitted		
9	Short circuit		No	t permitted		
10	False viewing direction		No	t permitted		
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk		According to the limit specimen			
13	Black /White spot(display)		Refer to item 1			
14	Black /White line(display)		Ref	fer to item 2		

Inspection items	Judgment standard
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_				Category(application: B zone)	Acceptable
					number
		i) The front of lead terminals	В	a≤t, b≤1/5W, c≤3mm Crack at two sides of lead terminals should not cover patterns and alignment mark	
15	Glass	Glass Gl	b	< Inner borderline of the seal	Max.3 defects
	crack	Inner border line of the seal Outer border line of the seal	b <	< Outer borderline of the seal	allowed
		iv) Corner	Α	a <= t, b <= 3.0, c <= 3.0	
		w b c	В	Glass crack should not cover patterns u and alignment mark and patterns.	

Inspection items	Judgment standard
inspection items	Category(application: B zone)

		<u> </u>	
		Component soldering: No cold soldering, short, open circuit, burr, tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2) lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	Component Soldering pad Lead L2>0 L1>0
16	PCB defect	Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	Soldering tin is not permit in this area Soldering tin is not permit in this area Socket Base Board
		Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.	Glue Lead PCB Insulative coat

10. Precautions for Use of LCD Modules

10.1 Handling Precautions

- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range.

 If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0° C $\sim 40^{\circ}$ C

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.