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Pre SPEC
FOR
LCD MODULE

Customer   : _____

Product Model: _____BD070DNB12_____

Sample code: _____

Designed by	Checked by	Approved by

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Revision History

Version	Contents	Date	Note
A	Original	2013.02.27	

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Contents

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2. Scope

This specification applies to the TFT LCD module

3. Normative Reference

GB/T4619-1996 《 Liquid Crystal Display Test Method》

GB/T2424 《 Basic environmental Testing Procedures for Electric and Electronic Products.》

GB/T2423 《Basic Testing Procedures for Electric and Electronic Products》

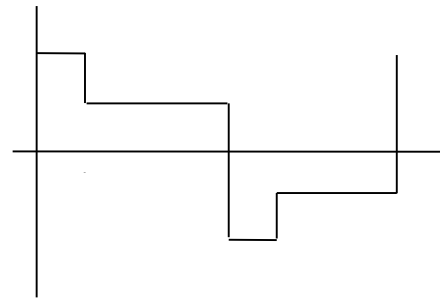IEC61747-1 《SIXTH PARTGB2828`2829-87 《National Standard of PRC》

4. Definitions

4.1 Definitions of Vop

The definitions of threshold voltage Vth1, Vth2 the following typical waveforms are applied on liquid crystal by the method of equalized voltage for each duty and bias.
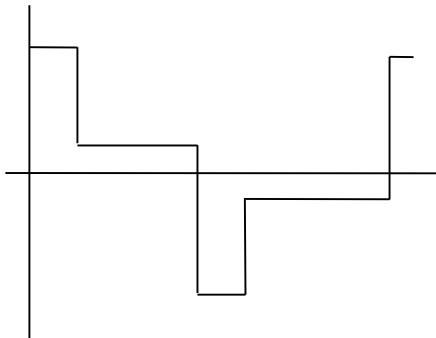
【 selected waveform 】　　　　　　　　【 non-selected waveform 】

① Vth1: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of selected waveform

$$(f_f=80Hz, \ \Phi=10°\ \theta=270° \ at \ 25℃)$$

② Vth2: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of non-selected waveform

$$(f_f=80Hz, \ \Phi=10°\ \theta=270° \ at \ 25℃)$$

③ Vop: (Vth1(50%)+Vth2(50%))/2 　　$(f_f=80Hz, \ \Phi=10°\ \theta=270° \ at \ 25℃)$

4.2 Definition of Response Time Tr, Td

①Tr: The time required which the brightness of segment becomes 10% from 100% when waveform is switched to selected one from non-selected one. ($f_f=80Hz, \ \Phi=10°\theta=270°$ at 25℃)

②Td: The time required which the brightness of segment becomes 90% from 10% when waveform is switched to selected one from selected one. ($f_f=80Hz, \ \Phi=10°\theta=270°$ at 25℃)
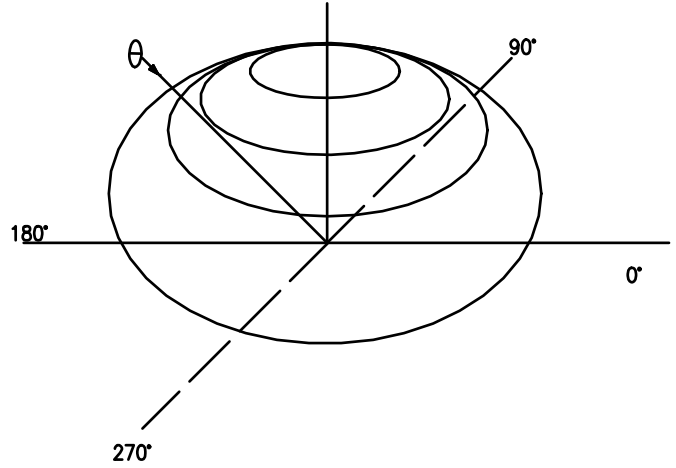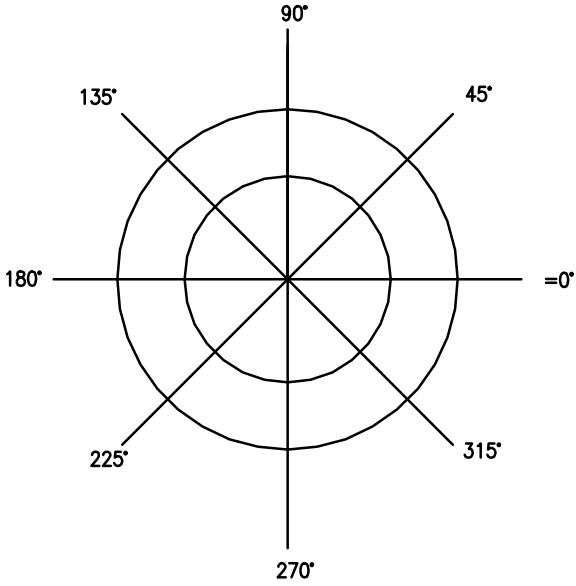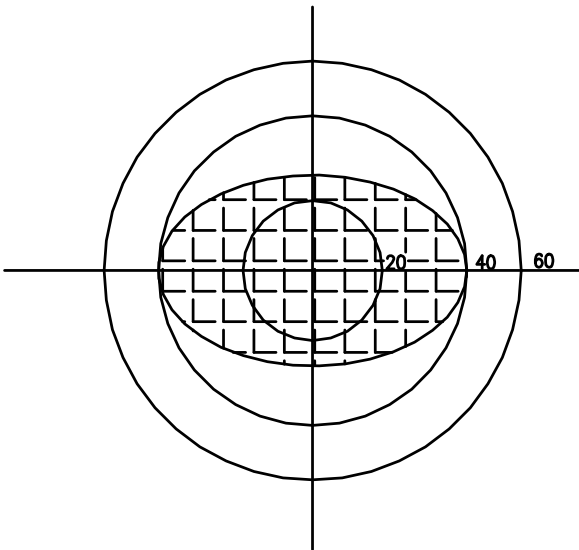
4.3 Definition of Contrast Ratio Cr

Cr=A/B

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① A: Segments brightness in case of non-selected waveform
② B: Segments brightness in case of selected waveform

4.4 Definition of Angle and Viewing Range

Angular Graph: Constrast Ratio

Such as:
Viewing Angle Range:
80(Cr>2) Horizontal
70(Cr>2) Vertical

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5. Block Diagram
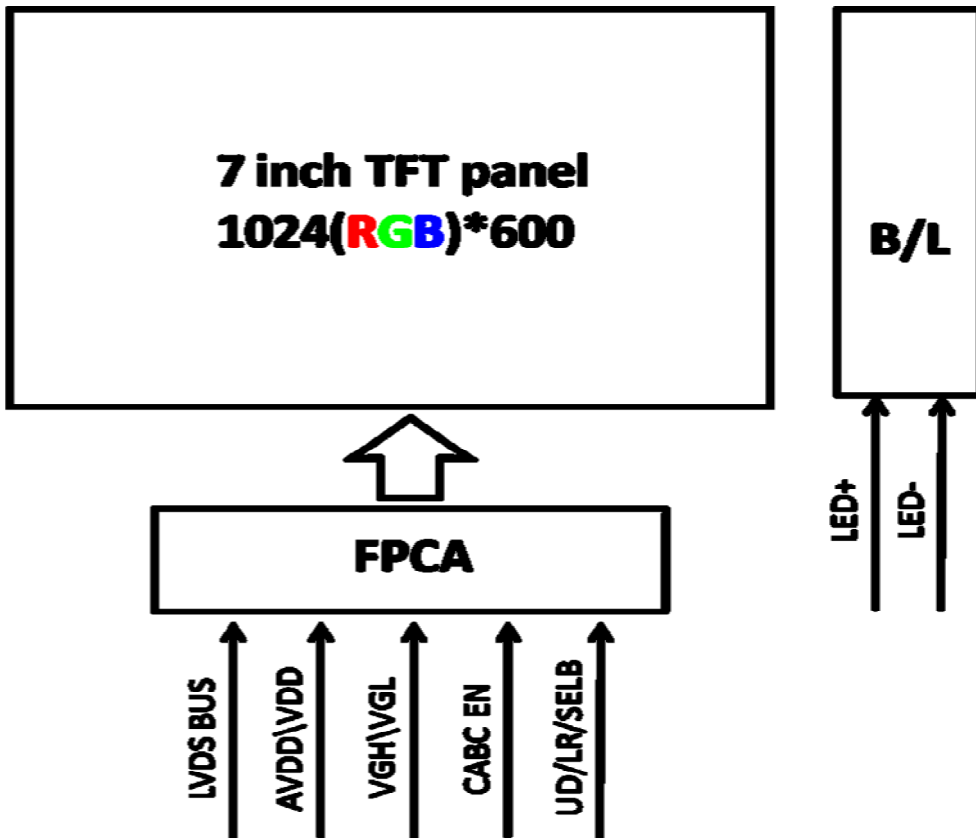
6. Technology Specifications

6.1 Features

This single-display module is suitable for use in Net Book products.

The LCD adopts one backlight with High brightness 24-lamps white LED.

Construction: 7″ a-Si color TFT-LCD ,White LED backlight, FPC and T-CON.

6.2 General Specifications

No.	Item	Specification
1	LCD size	7 inch
2	Resolution	1024 (RGB)X600
3	Display mode	Normally white, Transmissive
4	Pixel pitch	0.150(W)X0.150(H) mm
5	Active area	153.6 (W)X90.0 (H) mm
6	Module size	165.75(W)X105.39(H)X2.80(D)mm
7	Pixel arrangement	RGB-stripe
8	Interface	LVDS
9	Backlight power consumption	1.782W(Typ.)
10	Panel power consumption	TBD
11	Weight	TBD

6.3 Interface Pin Connection

Pin No.	Symbol	I/O	Function	Remarks
1	VCOM	P	Common Voltage	
2	VDD	P	Power Supply,3.3V(typical)	
3	VDD	P	Power Supply,3.3V(typical)	
4	NC	--	No Connection	
5	Reset	I	Global reset pin	
6	STBYB	I	Standby mode ,normally pulled high	Note 1
7	GND	P	Ground	
8	Rin0+	I	-LVDS differential data input(R0-R5,G0)	
9	Rin0+	I	+ LVDS differential data input(R0-R5,G0)	
10	GND	P	Ground	
11	Rin1-	I	- LVDS differential data input(G1-G5,B0-B1)	
12	Rin1+	I	+LVDS differential data input(G1-G5,B0-B1)	
13	GND	P	Ground	
14	Rin2-	I	- LVDS differential data input(B2-B5,HS,VS,DE)	
15	Rin2+	I	+LVDS differential data input(B2-B5,HS,VS,DE)	
16	GND	P	Ground	
17	Rxclkin-	I	-LVDS differential clock input	
18	Rxclkin+	I	+LVDS differential clock input	
19	GND	P	Ground	
20	Rin3-	I	-LVDS differential data input(R6-R6,G6-G7,B6-B7)	
21	Rin3+	I	-LVDS differential data input(R6-R6,G6-G7,B6-B7)	
22	GND	P	Ground	
23	NC	--	No Connection(Reserve)	
24	NC	--	No Connection(Reserve)	
25	GND	P	Ground	
26	NC	--	No Connection(Reserve)	
27	DIMO	O	Backlight CABC Controller signal output	
28	SELB	I	6bit/8bit mode select	Note 2
29	AVDD	P	Analog power	
30	GND		Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	L/R	I	Horizontal inversion	Note 3
34	U/D	I	Vertical inversion	Note 3
35	VGL	P	Gate off voltage	
36	CABCEN1	I	CABC enable	Note 4
37	CABCEN2	I	CABC enable	Note 4
38	VGH	P	Gate on voltage	
39	LED+	P	LED Anode	
40	LED+	P	Led Anode	

Note 1: STBYB=high ,normally operation; STBYB=low, source driver output .high-Z.

Note 2: If LVDS input data is 6bits,SELB must be set to high;
If LVDS input data is 8bits,SELB must be set to low.

Note 3: L/R&U/D scan direction setting:

Scan control		Scan direction
L/R	U/D	
High	Low	Left to right , up to down
High	High	Left to right , down to up
Low	High	Right to left , down to up
Low	Low	Right to left , up to down

Note 4: Normally pull low.
 When CABC_EN="00",CABC OFF;
 When CABC_EN="01",user interface image;
 When CABC_EN="10",still picture;
 When CABC_EN="11",moving image;
 When CABC off, don't connect DIMO, else connect it to backlight.

6.4 Absolute Max. Rating

Item	Symbol	Values		Unit
		Min.	Max.	
Power Voltage	VDD	-0.3	4.0	V
	AVDD	6.5	12	V
	VGH	-0.3	20	V
	VGL	-20	0.3	V
Backlight forward current	I_{LED}	0	25	mA(For each LED)
Input Signal Voltage	V_I	-0.3	VDD	
Operation Temperature	T_{OP}	0	50	℃
Storage Temperature	T_{ST}	-20	60	℃

6.5 Typical Operation Conditions

Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Power Voltage	VDD	3.0	3.3	3.6	V
	AVDD	10.7	10.85	11	V
	VGH	20	21	22	V
	VGL	-9	-8	-7	V
	VCOM	2.4	2.5	2.6	V
Power consumption	I_{VDD}	TBD	TBD	TBD	mA
	I_{AVDD}	TBD	TBD	TBD	mA
	I_{LED}	150	160	170	mA

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6.6 Power Sequence
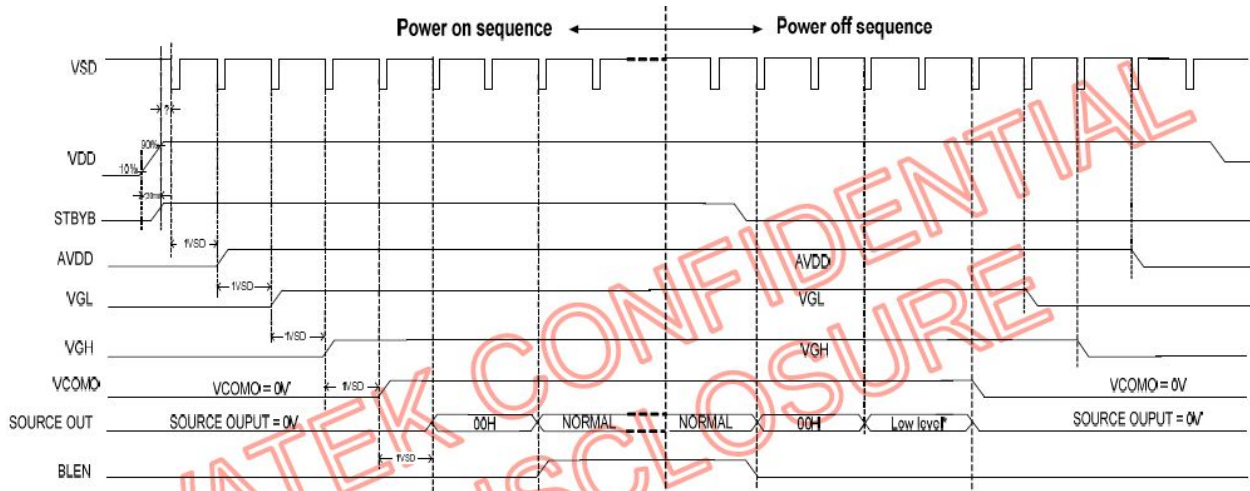

Note: SOURCE OUT includes Rin0- ~ Rin2-,Rin0- ~ Rin2-,Rin0+ ~ Rin2+,CLKIN- , CLKIN+.

6.7 Timing Conditions

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @ Frame rate = 60Hz	fclk	40.8	51.2	67.2	MHz
Horizontal display area	thd		1024		DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd		600		H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

Timing Diagram
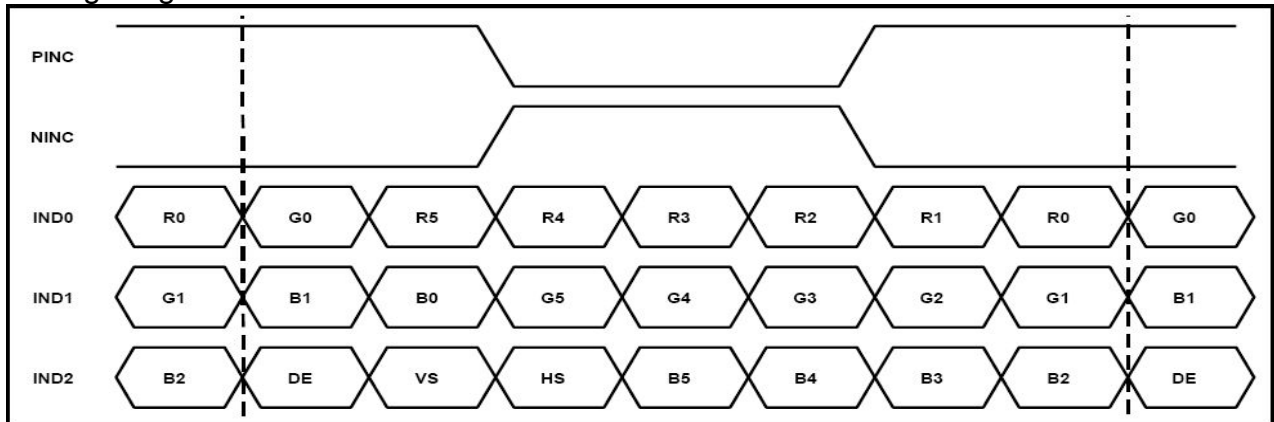
6bits LVDS data input

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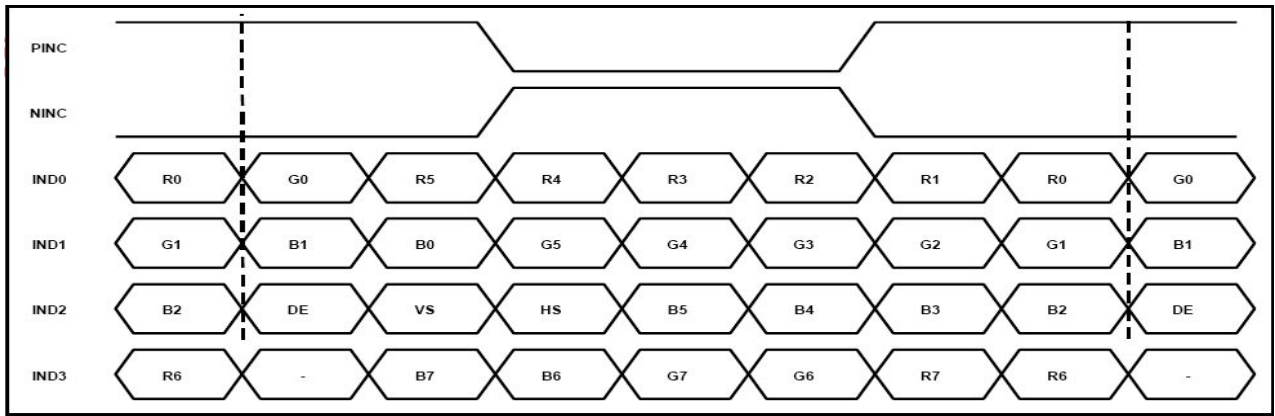
8bits LVDS data input

6.8 Optical specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θ_L	Φ=180°(9 o'clock)	-	75	-	degree	Note 1
	θ_R	Φ=0°(3 o'clock)	-	75	-		
	θ_T	Φ=90°(12 o'clock)	-	70	-		
	θ_B	Φ=270°(6 o'clock)	-	75	-		
Response time Rise+Fall	T_{RT}		-	20	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X	Normal θ=Φ=0°	0.26	0.31	0.36	-	Note 2
	W_Y		0.28	0.33	0.38	-	Note 5 Note 6
Luminance	L		-	250	-	-	Note 6
Luminance uniformity	Y_U		70	75	-	%	Note 6,7

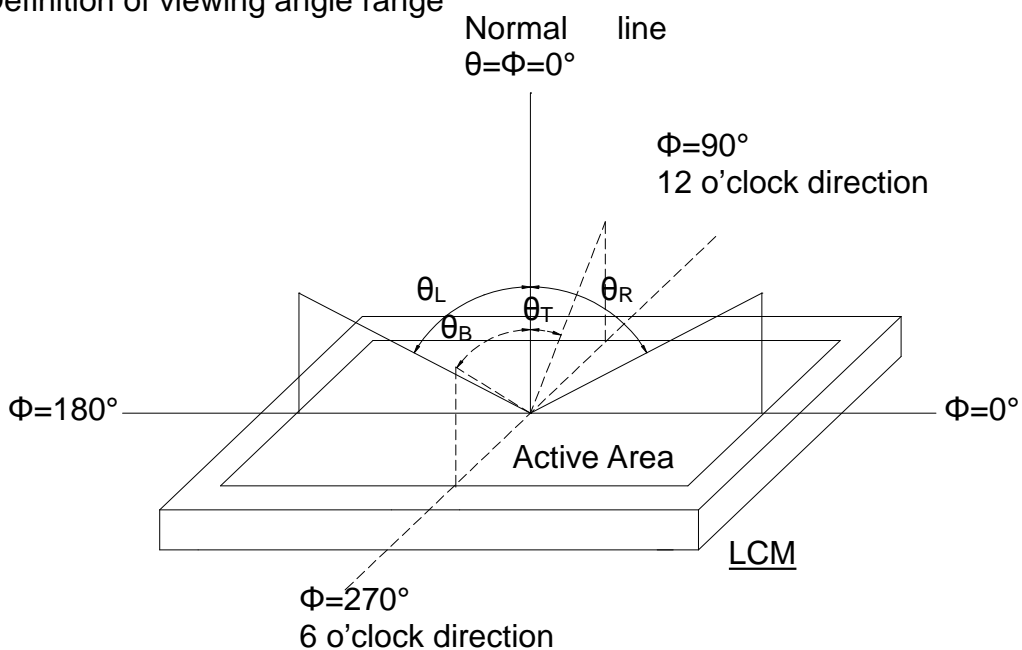Note 1: Definition of viewing angle range

Fig. 4-1 Definition of viewing angle

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Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by ELDIM-EZ contrast/Height :1.2mm ,Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

Normal line
$\theta=\Phi=0°$

Photo detector

500mm

$\Phi=90°$
12 o'clock direction

$\Phi=180°$

$\Phi=0°$

Active Area

$\Phi=270°$
6 o'clock direction

LCM
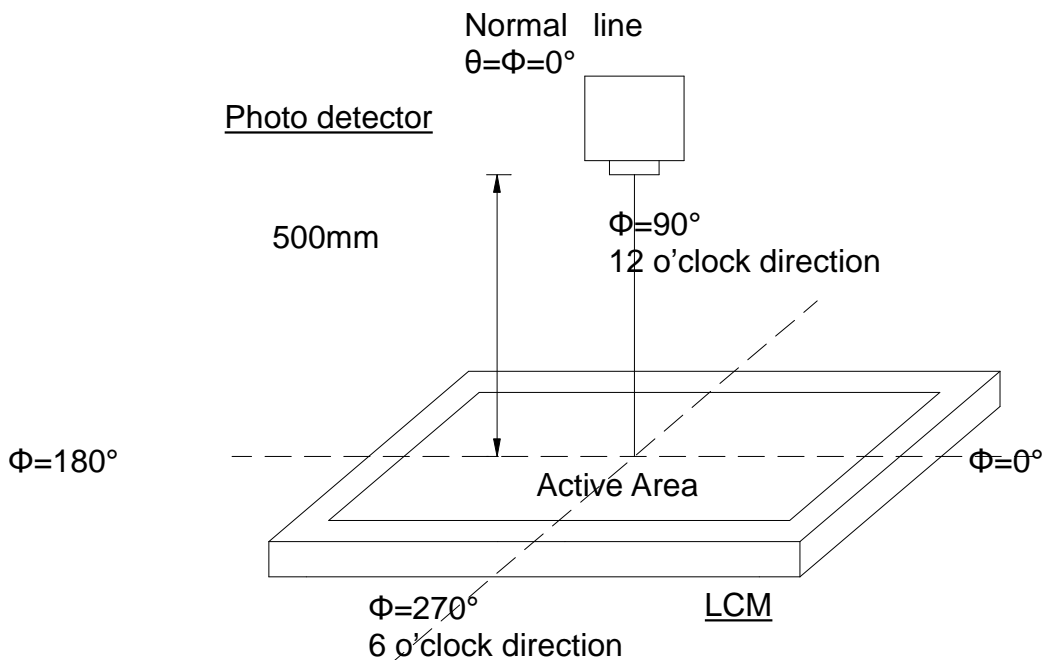
Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

| White (TFT OFF) | Black (TFT ON) | White (TFT OFF) |

Photo detector output (Relative value)

100%
90%

10%
0%

T_{ON}

T_{OFF}
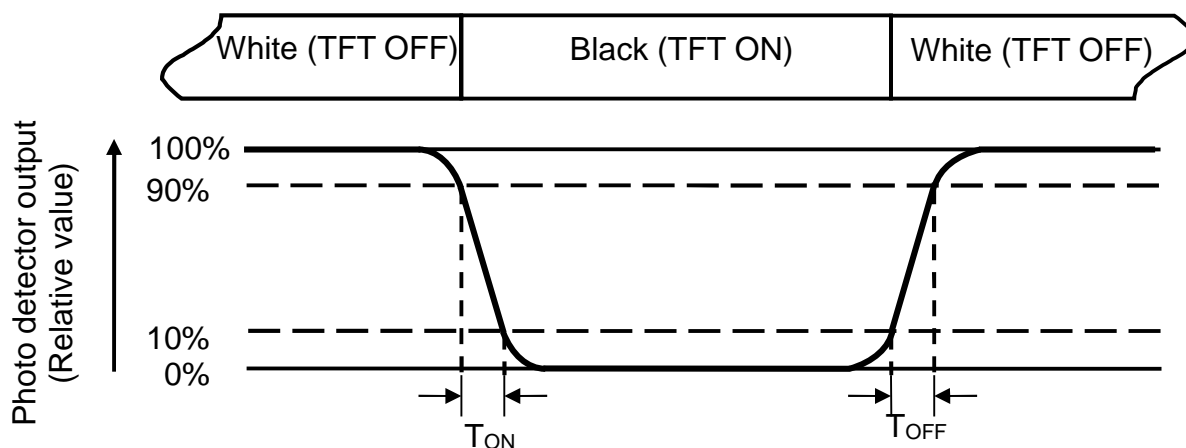
Fig. 4- 3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $V_{LED}=5.0V$.

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Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$Luminance\ Uniformity\ (Yu) = \frac{B_{min}}{B_{max}}$$

L-------Active area length   W----- Active area width
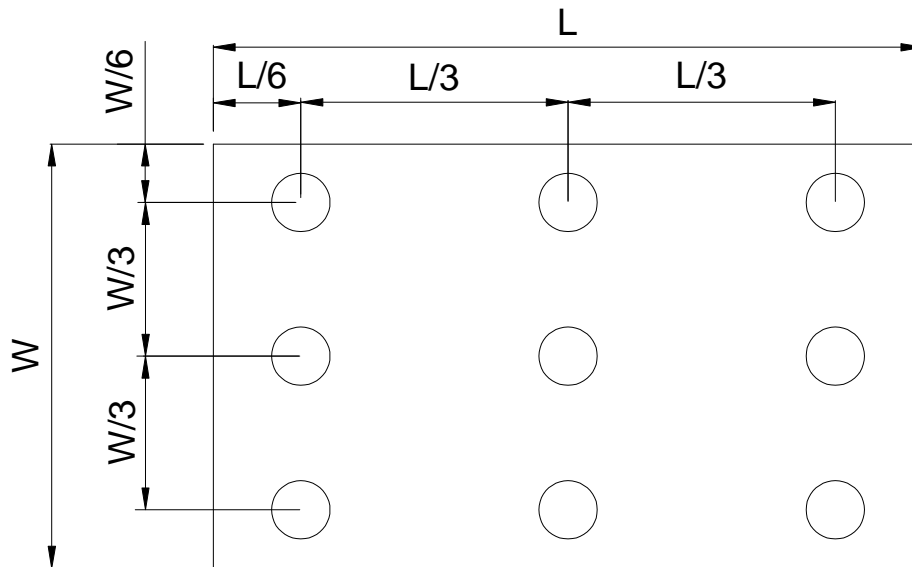

Fig. 4- 4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

7. Reliability Test Conditions And Methods

Item	Test Conditions	Remark
High Temperature Storage	Ta = 80℃ 240 hrs	
Low Temperature Storage	Ta =-30℃ 240hrs	
High Temperature Operation	Ts = 70℃ 240hrs	
Low Temperature Operation	Ta = -20℃ 240hrs	
Operate at High Temperature and Humidity	+60℃, 90%RH max. 240 hrs	Operation
Thermal Shock	-20℃~ +70℃ 100 cycles 2Hrs/cycle	Non-operation
Electrostatic Discharge	Contact=±4KV, class B Air=±8KV, class B	

8. Handling Precautions

8.1 Mounting method

The LCD panel of Daxian LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.
Extreme care should be needed when handling the LCD modules.

8.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent
[recommended below] and wipe lightly
- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:
- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:
- Soldering flux
- Chlorine (CI) , Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

8.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

8.4 packing

- Module employ LCD elements and must be treated as such.

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- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

8.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  Usage under the maximum operating temperature, 50%Rh or less is required.

8.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.
- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it . And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
  [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

8.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

9. Precaution for use

9.1
A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

9.2
On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.
- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to Daxian , and some problem is arisen in this specification due to the

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● When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

10. Package Drawing

1. Bend FPC and Light bar wire to LCM backside.
2. Fix with crepe paper tape .
3. Put LCM into A/S BAG .

11. Outline Dimension

PIN	DESCRIPTION
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	DIMO
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	VGL
36	CABCEN1
37	CABCEN0
38	VGH
39	LED+
40	LED+

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MODEL NO: BD070DNB12

PART NO:

TITLE: LCM模组图

REV 版本	DESCRIPTION 描述	DATE 日期
A00	First issue	2013-02-27

SCALE: FIT
SHEET: 1 OF 1
GENERAL TOL: ±0.2MM

APP: DATE:
CHK: DATE:
DWN: Annan DATE: 2013-02-27

4.50±0.50
29.57
*2.80MAX
0.30±0.05

*105.39±0.30(OUTLINE)
*45.06±0.50
3.15
92.60(V.A)
9.64
90.00(A.A.)
5.98
7.28
4.45

*165.75±0.30(OUTLINE)
156.20(V.A)
153.60(A.A.)

7.0" TFT
1024X600

9.50±0.20
5.46
3.50±0.30

single LAYER
7.00
20.00±0.20
*67.81±0.50
P0.5*(40-1)=19.50
20.50±0.10

A
K

Backlight LED Circuit

Specification:
1). Luminance(cd/m2): 250(TYP)
2). Operating temp: -10°C~+60°C
   Storage temp: -20°C~+70°C
3). All the raw material are Rohs complicant,上下铁框材质马口铁
4). Dimensions with mark "*" are important, with mark "()" are reference.